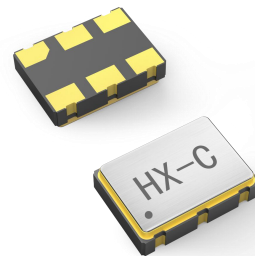


差分晶振7S系列 7050



APPLICATIONS

- SONET / SDH / Gbits / Ethernet
- Graphics/ Radar/ HD Monitoring Devices
- Low Phase Jitter:0.5psec,Typical

FEATURE

- LVDS LVPECL HCSL
- Wide Frequency Range

- Low Operating Voltage:1.8V/2.5V/3.3V
- Tri-state Available

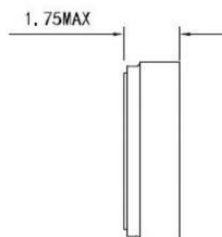
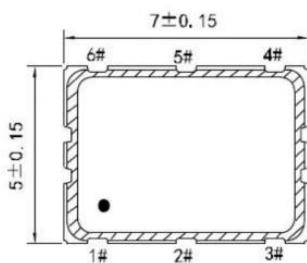


Electrical Specifications 电气参数

项目	Item/Type	Electrical Specifications
频率范围	Frequency Range	10MHz to 250MHz
输出	Output	LVDS, LVPECL, HCSL
电源电压	Supply Voltage	1.8V, 2.5V, 3.3V(±10%)
频率稳定度	Frequency Stability	±20ppm
工作温度	Operating Temperature Range	-40°C ~ +85°C
消耗电流	Current Consumption	100mA max.
输出负载	Output Load	100Ω between output and complementary output
占空比	Symmetry	45% to 55%
启动时间	Start-up Time	3ms max.
上升/下降时间	Rise/ Fall Time	1ns max.
启动/禁用时间	Output Enable/ Disable Time	Enable:10ms max. Disable:0.2μs max.
输出电平-高	Output Logic High "1 "	70%VDD.
输出电平-低	Output Logic Low "0 "	30%VDD.
相位抖动	Phase Jitter	0.5psec . @12KHz to 20MHz
储存温度范围	Storage Temperature Range	-55°C ~ +125°C
老化率	Aging (at 25°C)	±2ppm/year max.

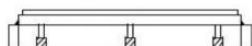
Mechanical Dimensions 外型尺寸

Units:mm

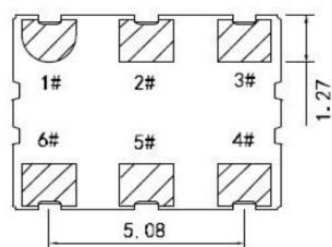


Pad Functions:

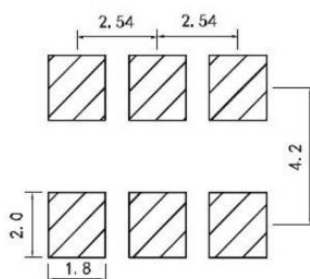
1. EN
2. NC
3. GND
4. Output
5. Comp. Output
6. Vdd



■ Pad size 焊盘尺寸



Bottom View



Suggested solder pad layout